



**INDIAN INSTITUTE OF TECHNOLOGY TIRUPATI**  
**Timetable (Jan-June 2019 Semester)**

**ELECTRICAL ENGINEERING**  
**8<sup>th</sup> Semester - IV Year B.Tech.**

DAY	1	2	3	4		5	6	7	8
	9am	10am	11am	12pm		2pm	3pm	4pm	5pm
Mon	A	B	C	D	Lunch recess 1-2 pm	D			
Tue		C		C					
Wed		E		E					
Thu	C	D	E	A		A			
Fri	D	B	A						

Slot	Course No and Title	Faculty	Room No
---	EC4201 Project II (- - - 9)	<i>Dr. Rama Krishna Gorthi</i>	---
B	HS3050 Professional Ethics (2-0-0-2)	<i>Dr. Bharath Kumar</i>	3
A	Department Elective 5 (3-0-0-3)		
A	EE5027 Linear Integrated Circuits Theory and Applications	<i>Dr. Prashanth Vooka</i>	1
A	EE5204 Statistical Signal Processing (3-0-0-3)	<i>Dr. Parthajit Mohapatra</i>	2
C	EE5028 CAD for VLSI Systems	<i>Dr. Vikramkumar Pudi</i>	1
C	EE5102 Linear Dynamical Systems	<i>Dr. P. S. Sai Krishna</i>	5
D	EE5025 Information Theory and Coding	<i>Dr. Parthajit Mohapatra</i>	2
E	CS5025 Predictive Data Modeling	<i>Dr. Ravi Prakash Iyer</i>	4

\* Each slot is of 50 minutes' duration.