

INDIAN INSTITUTE OF TECHNOLOGY TIRUPATI

Timetable (July-Dec 2021 Semester)

ELECTRICAL ENGINEERING I Semester - I Year M.Tech (Microelectronics and VLSI)

Day	9-9:50	10-10:50	11-11:50	12-12:50	12:50-14	14-14:50	15-17:30
Mon	A	C	E	F	LUNCH		P
Tue	B	D	A			A(T)	
Wed	C	E	B	F			Institute Seminar
Thu	D	A	C				R
Fri	E	B	D	F			

Slot	Course #	Course Title	L-T-P-C	Instructor(s)
A	EE5039	Physics and Modelling of Semiconductor Devices	3-1-0-4	<i>Dr. Vijay</i>
B	MAE1 MA5021	Mathematics Elective Linear Algebra for Engineers	2-0-0-2	<i>Dr. Sumit Giri</i>
B	MA5022	Probability Theory for Engineers	2-0-0-2	<i>Dr. Sumit Giri</i>
D	MA5023	Differential Equation for Engineers	2-0-0-2	<i>Dr. Panchatcharam Mariappan</i>
C	EE5036	VLSI Fabrication Principles	3-0-0-3	<i>Dr. Swapnil</i>
E	EE5109	Digital VLSI Design	3-0-0-3	<i>Dr. N.N Murthy</i>
F/P	DPE1 EE5035	Embedded Systems	2-0-2-3	<i>Dr. Prashant Vooka</i>
R	EE5195	Device Simulation Laboratory	0-0-3-2	<i>Dr. Vijay</i>